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1 Parallel execution of prolog programs: a survey

Gopal Gupta, Enrico Pontelli, Khayri A.M. Ali, Mats Carlsson, Manuel V. Her

July 2001 **Transactions on Programming Languages and Systems (**

Publisher: ACM

Full text available: Pdf (1.95 MB) Additional Information: full citation, abstract, re

Bibliometrics: Downloads (6 Weeks): 24, Downloads (12 Months): 226, Citatio

Since the early days of logic programming, researchers in the field reali exploitation of parallelism present in the execution of logic programs. TI presence of nondeterminism, and their referential transparency, ...

Keywords: Automatic parallelization, constraint programming, logic pr prolog

2 Communications of the ACM: Volume 52 Issue 5

May 2009 Communications of the ACM

Publisher: ACM

Full text available: Digital Edition , Pdf (7.31 MB) Additional Information: full citation

Bibliometrics: Downloads (6 Weeks): 3429, Downloads (12 Months): 3429, Cit

3 JIST: Just-In-Time scheduling translation for parallel processors

Giovanni Agosta, Stefano Crespi Reghizzi, Gerlando Falauto, Martino Sykor

July 2005 **Scientific Programming** , Volume 13 Issue 3

Publisher: IOS Press

Additional Information: full citation, abstract, references

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citatio

The application fields of bytecode virtual machines and VLIW processors embedded and mobile systems, where the two technologies offer differe portability, low power consumption and reduced hardware ...

4 Communications of the ACM: Volume 51 Issue 10

October 2008 Communications of the ACM

Publisher: ACM

Full text available: Digital Edition , Pdf (7.16 MB) Additional Information: full citation

Bibliometrics: Downloads (6 Weeks): 648, Downloads (12 Months): 2959, Cita

5

Formal verification of translation validators: a case study on instructi
Jean-Baptiste Tristan, Xavier Leroy
January 2008 **POPL '08:** Proceedings of the 35th annual ACM SIGPLAN-SIG of programming languages
Publisher: ACM
Full text available: Pdf (257.55 KB) Additional Information: full citation, abstract, re

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 131, Citation

Translation validation consists of transforming a program and *a posteric* detect a modification of itssemantics. This approach can be used in a ve validation is formally proved to be correct. ...

Keywords: scheduling optimizations, the coq proof assistant, translatic compilers

Also published in:
January 2008 **SIGPLAN Notices** Volume 43 Issue 1

6 Memory model sensitive bytecode verification
Thuan Quang Huynh, Abhik Roychoudhury
December 2007 **Formal Methods in System Design** , Volume 31 Issue 3
Publisher: Kluwer Academic Publishers
Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citatio

Modern concurrent programming languages like C# and Java have a pr memory model, which captures the set of all allowed behaviors of progr platform--uni- or multi-processor. Such a memory model is typically ...

Keywords: Bytecode verification, Programming language memory mod

7 Vertical profiling: understanding the behavior of object-priented appli
Matthias Hauswirth, Peter F. Sweeney, Amer Diwan, Michael Hind
October 2004 **OOPSLA '04:** Proceedings of the 19th annual ACM SIGPLAN programming, systems, languages, and applications
Publisher: ACM
Full text available: Pdf (1.16 MB) Additional Information: full citation, abstract, re

Bibliometrics: Downloads (6 Weeks): 10, Downloads (12 Months): 92, Citation

Object-oriented programming languages provide a rich set of features t software engineering benefits. The increased productivity provided by tl justifiable cost in a more sophisticated runtime system whose ...

Keywords: hardware performance monitors, perturbation, software pe profiling, whole-system analysis

Also published in:
October 2004 **SIGPLAN Notices** Volume 39 Issue 10

8

Power reduction techniques for microprocessor systems
Vasanth Venkatachalam, Michael Franz
September 2005 **Computing Surveys (CSUR)** , Volume 37 Issue 3
Publisher: ACM
Full text available: Pdf (602.33 KB) Additional Information: full citation, abstract, re

Bibliometrics: Downloads (6 Weeks): 68, Downloads (12 Months): 727, Citatio

Power consumption is a major factor that limits the performance of com of the art" in techniques that reduce the total power consumed by a mic time. These techniques are applied at various ...

Keywords: Energy dissipation, power reduction

9 A survey of processors with explicit multithreading
Theo Ungerer, Borut Robič, Jurij Šilc
March 2003 **Computing Surveys (CSUR)** , Volume 35 Issue 1
Publisher: ACM
Full text available: Pdf (920.16 KB) Additional Information: full citation, abstract, re

Bibliometrics: Downloads (6 Weeks): 60, Downloads (12 Months): 537, Citatio

Hardware multithreading is becoming a generally applied technique in t microprocessors. Several multithreaded processors are announced by ir production in the areas of high-performance microprocessors, media, ..

Keywords: Blocked multithreading, interleaved multithreading, simulta

10 Global instruction scheduling in dynamic compilation for embedded s
Giovanni Agosta, Stefano Crespi Reghizzi, Dario Domizioli, Martino Sykora
October 2006 **JTRES '06:** Proceedings of the 4th international workshop on time and embedded systems
Publisher: ACM
Full text available: Pdf (322.56 KB) Additional Information: full citation, abstract, re

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 25, Citation (

The application fields of bytecode virtual machines and Very Long Instru overlap in the area of embedded and mobile systems, where the two te benefits, namely high code portability, low power consumption ...

Keywords: instruction scheduling, java, just-in-time compilation, very

11 Formally verifying information flow type systems for concurrent and t
Gilles Barthe, Leonor Prensa Nieto
October 2004 **FMSE '04:** Proceedings of the 2004 ACM workshop on Formal engineering
Publisher: ACM
Full text available: Pdf (206.04 KB) Additional Information: full citation, abstract, re

Bibliometrics: Downloads (6 Weeks): 8, Downloads (12 Months): 34, Citation (

Information flow type systems provide an elegant means to enforce con the proof assistant Isabelle/HOL, we have machine-checked a recent wo cite BC02:tcs, which defines an information flow type ...

Keywords: concurrency, machine-checked proofs, noninterference

12 A region-based compilation technique for a Java just-in-time compile

Toshio Suganuma, Toshiaki Yasue, Toshio Nakatani

June 2003 **PLDI '03:** Proceedings of the ACM SIGPLAN 2003 conference o design and implementation

Publisher: ACM

Full text available: Pdf (158.62 KB) Additional Information: full citation, abstract, re

Bibliometrics: Downloads (6 Weeks): 12, Downloads (12 Months): 83, Citation

Method inlining and data flow analysis are two major optimization comp transformations, however they often suffer from the existence of rarely contained in the target method. One major problem lies in ...

Keywords: dynamic compilers, on-stack replacement, partial inlining, r

Also published in:

May 2003 **SIGPLAN Notices** Volume 38 Issue 5

13 Fault-safe code motion for type-safe languages

Brian R. Murphy, Vijay Menon, Florian T. Schneider, Tatiana Shpeisman, Al

April 2008 **CGO '08:** Proceedings of the sixth annual IEEE/ACM internatior generation and optimization

Publisher: ACM

Full text available: Pdf (421.78 KB) Additional Information: full citation, abstract, re

Bibliometrics: Downloads (6 Weeks): 22, Downloads (12 Months): 155, Citatio

Compilers for Java and other type-safe languages have historically work and constraints imposed by runtime safety checks and precise exceptior exploit these safety properties to perform code motion optimizations ...

Keywords: code motion, intermediate representations, partial redunda motion, safety dependences, scheduling, speculative code motion

14 Dynamic detection of atomic-set-serializability violations

Christian Hammer, Julian Dolby, Mandana Vaziri, Frank Tip

May 2008 **ICSE '08:** Proceedings of the 30th international conference on

Publisher: ACM

Full text available: Pdf (349.88 KB) Additional Information: full citation, abstract, re

Bibliometrics: Downloads (6 Weeks): 27, Downloads (12 Months): 206, Citatio

Previously we presented *atomic sets*, memory locations that share some *units of work*, code fragments that preserve consistency of atomic sets We also proposed *atomic-set serializability* ...

Keywords: atomicity, concurrent object-oriented programming, data ra serializability

15 Sun Labs-The First Five Years: The First Fifty Technical Reports. A
Ching-Chih Chang, Amy Hall, Jeanie Treichel
September 1998 Sun Labs-The First Five Years: The First Fifty Technical Rep
Publisher: Sun Microsystems, Inc.
Full text available: Pdf (6.34 MB) Additional Information: full citation, abstract

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 6, Citation C

This commemorative issue in the technical report series encompasses tl existence from 1991 through 1996. In addition to the Abstracts of the fi include a list of patents issued during that ...

16 Constraint Programming: Strategic Directions
Pascal Van Hentenryck, Vijay Saraswat
April 1997 **Constraints** , Volume 2 Issue 1
Publisher: Kluwer Academic Publishers
Full text available: Publisher Site Additional Information: full citation, references, citec

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citatio

17 Strategic directions in constraint programming
Pascal Van Hentenryck, Vijay Saraswat
December 1996 **Computing Surveys (CSUR)** , Volume 28 Issue 4
Publisher: ACM
Full text available: Pdf (402.08 KB) Additional Information: full citation, references

Bibliometrics: Downloads (6 Weeks): 23, Downloads (12 Months): 186, Citatio

18 Inducing heuristics to decide whether to schedule
John Cavazos, J. Eliot B. Moss
June 2004 **PLDI '04:** Proceedings of the ACM SIGPLAN 2004 conference o design and implementation
Publisher: ACM
Full text available: Pdf (352.40 KB) Additional Information: full citation, abstract, re

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 62, Citation (

Instruction scheduling is a compiler optimization that can improve progi 10% or more, but it can also be expensive. Furthermore, time spent op a Java just-in-time (JIT) compiler than in a traditional ...

Keywords: Java, Jikes RVM, compiler optimization, instruction scheduli supervised learning

Also published in:
June 2004 **SIGPLAN Notices** Volume 39 Issue 6

19 Memory performance attacks: denial of memory service in multi-core
Thomas Moscibroda, Onur Mutlu

August 2007 **SS'07:** Proceedings of 16th USENIX Security Symposium on U
Publisher: USENIX Association
Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citatio

We are entering the multi-core era in computer science. All major high-
manufacturers have integrated at least two cores (processors) on the sa
that chips with many more cores will become widespread in ...

20 Implementation of a Coarse-Grained Reconfigurable Media Process
B. Mei, B. Sutter, T. Aa, M. Wouters, A. Kanstein, S. Dupont
June 2008 **Journal of Signal Processing Systems** , Volume 51 Issue 3
Publisher: Kluwer Academic Publishers
Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citatio

Architecture for Dynamically Reconfigurable Embedded Systems (ADRES
grained reconfigurable processor architecture. It targets at embedded a
high-performance, low-power and high-level language programmability.

Keywords: FPGA, H.264/AVC decoder, VLIW, embedded system, multi
architecture

Re

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